<package schemaVersion=**"1.2"** xmlns:xs=**"http://www.w3.org/2001/XMLSchema-instance"** xs:noNamespaceSchemaLocation=**"PACK.xsd"**>

<name>**CMSIS**</name>

<description>**Cortex Microcontroller Software Interface Standard (CMSIS) CORE, DSP, RTOS, Driver**</description>

<vendor>**ARM**</vendor>

<!-- <license>CMSIS\CMSIS\_END\_USER\_LICENCE\_AGREEMENT.rtf</license> -->

<url>**http://www.keil.com/pack/**</url>

<taxonomy>

<description Cclass=**"Board Support"**>**Generic Interfaces for Evaluation and Development Boards**</description>

<description Cclass=**"CMSIS"** doc=**"CMSIS\Documentation\General\html\index.html"**>**Cortex Microcontroller Software Interface Components**</description>

<description Cclass=**"Device"** doc=**"CMSIS\Documentation\Core\html\index.html"**>**Startup, System Setup**</description>

<description Cclass=**"Drivers"** doc=**"CMSIS\Documentation\Driver\html\index.html"**>**Unified Device Drivers**</description>

<description Cclass=**"File System"**>**File Drive Support and File System**</description>

<description Cclass=**"Network"**>**Network Stack using IP descriptions**</description>

<description Cclass=**"USB"**>**Universal Serial Bus Stack**</description>

</taxonomy>

<releases>

<release version=**"4.0.0"**>

**- CMSIS-Driver 2.00 Preliminary (incompatible update)**

**- CMSIS-Pack 1.10 Preliminary**

**- CMSIS-DSP 1.4.2 (see revision history for details)**

**- CMSIS-Core 3.30 (see revision history for details)**

**- CMSIS-RTOS RTX 4.74 (see revision history for details)**

**- CMSIS-RTOS API 1.02 (unchanged)**

**- CMSIS-SVD 1.10 (unchanged)**

</release>

</releases>

<!-- conditions are dependency rules that can apply to a component or an individual file -->

<conditions>

<condition id=**"CMSIS Core"**>

<description>**CMSIS CORE processor and device specific Startup files**</description>

<require condition=**"Cortex-M Device"**/>

<require Cclass=**"Device"** Cgroup=**"Startup"**/>

</condition>

<condition id=**"CM0\_LE\_GCC"**>

<description>**Cortex-M0 or Cortex-M0+ or SC000 processor based device in little endian mode for the GCC Compiler**</description>

<accept Dcore=**"Cortex-M0"**/>

<accept Dcore=**"Cortex-M0+"**/>

<accept Dcore=**"SC000"**/>

<require Dendian=**"Little-endian"**/>

<require Tcompiler=**"GCC"**/>

</condition>

</conditions>

<components>

<!-- CMSIS-Core component -->

<component Cclass=**"CMSIS"** Cgroup=**"CORE"** Cversion=**"3.30.0"** condition=**"CMSIS Core"**>

<description>**CMSIS-CORE for Cortex-M, SC000, and SC300**</description>

<files>

<!-- CPU independent -->

<file category=**"doc"** name=**"CMSIS\Documentation\Core\html\index.html"**/>

<file category=**"include"** name=**"CMSIS\Include\"**/>

</files>

<!-- CMSIS-DSP component -->

<component Cclass=**"CMSIS"** Cgroup=**"DSP"** Cversion=**"1.4.2"** condition=**"CMSIS DSP"**>

<description>**CMSIS-DSP Library for Cortex-M, SC000, and SC300**</description>

<files>

<!-- CPU independent -->

<file category=**"doc"** name=**"CMSIS\Documentation\DSP\html\index.html"**/>

<!-- <file category="header" name="CMSIS\Include\arm\_common\_tables.h"/> -->

<file category=**"header"** name=**"CMSIS\Include\arm\_math.h"**/>

<!-- CPU and Compiler dependent -->

<!-- ARMCC -->

<file category=**"library"** condition=**"CM0\_LE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM0l\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM0\_BE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM0b\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM3\_LE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM3l\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM3\_BE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM3b\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM4\_LE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM4l\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM4\_BE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM4b\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM4F\_LE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM4lf\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<file category=**"library"** condition=**"CM4F\_BE\_ARMCC"** name=**"CMSIS\Lib\ARM\arm\_cortexM4bf\_math.lib"** src=**"CMSIS\DSP\_Lib\Source\ARM"**/>

<!-- GCC -->

<file category=**"library"** condition=**"CM0\_LE\_GCC"** name=**"CMSIS\Lib\GCC\libarm\_cortexM0l\_math.a"** src=**"CMSIS\DSP\_Lib\Source\GCC"**/>

<file category=**"library"** condition=**"CM3\_LE\_GCC"** name=**"CMSIS\Lib\GCC\libarm\_cortexM3l\_math.a"** src=**"CMSIS\DSP\_Lib\Source\GCC"**/>

<file category=**"library"** condition=**"CM4\_LE\_GCC"** name=**"CMSIS\Lib\GCC\libarm\_cortexM4l\_math.a"** src=**"CMSIS\DSP\_Lib\Source\GCC"**/>

<file category=**"library"** condition=**"CM4F\_LE\_GCC"** name=**"CMSIS\Lib\GCC\libarm\_cortexM4lf\_math.a"** src=**"CMSIS\DSP\_Lib\Source\GCC"**/>

<!-- G++ -->

<file category=**"library"** condition=**"CM0\_LE\_G++"** name=**"CMSIS\Lib\G++\libarm\_cortexM0l\_math.a"** src=**"CMSIS\DSP\_Lib\Source\G++"**/>

<file category=**"library"** condition=**"CM3\_LE\_G++"** name=**"CMSIS\Lib\G++\libarm\_cortexM3l\_math.a"** src=**"CMSIS\DSP\_Lib\Source\G++"**/>

<file category=**"library"** condition=**"CM4\_LE\_G++"** name=**"CMSIS\Lib\G++\libarm\_cortexM4l\_math.a"** src=**"CMSIS\DSP\_Lib\Source\G++"**/>

<file category=**"library"** condition=**"CM4F\_LE\_G++"** name=**"CMSIS\Lib\G++\libarm\_cortexM4lf\_math.a"** src=**"CMSIS\DSP\_Lib\Source\G++"**/>

</files>

</component>

</components>

</package>